

List of published papers

Simulation algorithms

Foreign conferences

Litovski, V., Petković, P., "*Time Domain Black-Box Modelling Of CMOS Structures And Analog Timing Simulation*", 3rd Annual European Computer Conf., COMPEURO '89, Hamburg, May, 1989, pp. 5.142-5.143.

Mrčarica, Ž., Litovski, V., "*Some Considerations Of Short-Channel MOS Model Selection For An Analog Circuit Simulation Program*", Microsystem '92, Bratislava, 17-19. November, 1992.

Maksimović, D., Glozić, D., Litovski, V., "*Logic Simulation Modeling Techniques In Hybrid Simulator Alecsis2.0*", 15-16 International Annual School on Semiconductor and Hybrid Technologies, 1992-93, Sozopol, May, 1994, pp. 133-139.

Zwolinski, M., Litovski, V., "*Hybrid Simulation: The State-Of-The-Art*", 2nd Serbian Conference on Microelectronics and Optoelectronics, MIOPEL '93, Niš, Srbija, 26-28 Oktobar, 1993, pp. 375-382.

Maksimović, D., Glozić, D., Litovski, V., "*Discrete Event Modeling And Simulation In An Object Oriented Hybrid Simulator*", XIV IASTED International Conference: Modelling Identification and Control, Innsbruck, Feb., 1995, pp. 103-106.

Mrčarica, Ž., Ilić, T., Glozić, D., Detter, H., Litovski, V., "*Mechatronics Simulation Using Alecsis, Anatomy Of Simulator*", EUROSIM '95, Vienna, 1995, pp. 601-604.

Mrčarica, Ž., Glozić, D., Ilić, T., Litovski, V., "*Capacitive Preassure Sensor For Quasi-Static Conditions Followed By A/D Conversion*", 20th International Conf. on Microelectronics MIEL '95, Vol. 2, Niš, 12.-14. Sep., 1995, pp. 601-604.

Mrčarica, Ž., Glozić, D., Litovski, V., Detter, H., "*Simulation Of Microsystems Using A Behavioural Hybrid Simulator Alecsis*", First Int. Conf. on Microsystems and Microstructures, MICROSIM'95, Southampton, 26-28 Sep., 1995, pp. 129-136. Printed also in: Adey, R.A., Lahrman, A., Lemboe Ilmann, C., editors, "Simulation and Design of Microsystems and Microstructures", Computational Mechanics Publication, Southampton, GB, 1995.

Mrčarica, Ž., Detter, H., Glozić, D., Litovski, V., "*Describing Space-Continous Models Of Microelectromechanical Devices For Behavioural Simulation*", European Design Automation Conference, EURO-DAC '96 with EURO-VHDL '96, Geneva, 16-20 Sep., 1996, pp. 316-321.

Mrčarica, Ž., Randelović, Z., Jakovljević, M., Litovski, V., Detter, H., "*Methods For Description Of Microelectromechanical Device Models For System-Level Simulation*", in Adey, R.A. and Renaud, Ph., ed. "Microsim II, Sec. Inter. Conf. on the Simulation and Design of Microsystems and Microstructures, Proceed.", Lausanne, Sep., 1997, pp. 271-280.

Mrčarica, Ž., Litovski, V., Ilić, T., "*Simulation Of The Pressure Sensor Interaction With Its Electronic Environment*", Micromechanics Europe 1977, Eighth Workshop on Micromachining, Micromechanics and Microsystems, MME '97, Southampton, Kingdom, 31 Aug. - 2 Sep., 1997, pp. 246-249.

Aleksić, D., Litovski, V., Milenković, S., "*TCP/IP Protocol Modeling And Simulation Using Alecsis 2.3*", 3rd Int. Conf. on Telecommunications in Modern Satellite, Cable and Broadcasting Services TELSIKS '97, Niš, Oct., 1997, pp. 564-567.

Mrčarica, Ž., Risojević, V., Lenczner, M., Jakovljević, M., Litovski, V., "*Integrated Simulator For Mems Using FEM Implementation In AHDL And Frontal Solver For Large Sparse Systems Of Equations*", Design, Test and Microfabrication of MEMS, MOEMS '98, CAD, Design and Test, Paris, 30. Mar. - 1. Apr., 1999, pp. 306-315.

Maksimović, D., Litovski, V., "*Inverse Circuit Simulation Method For Topological Delays Estimation With Logic Simulator*", 22nd Int. Conf. on Microelectronics, MIEL '99, Niš, May, 2000, pp. 707-710.

Jakovljević, M., Mrčarica, Ž., Fotiu, P., Detter, H., Litovski, V., "*Analogue Behavioural Simulator As A Tool For Coupled Electro-Thermal Analysis Of Microsystems*", Proc. of the 22nd Int. Conf. on Microelectronics, MIEL '99, Niš, Yugoslavia, May, 2000, pp. 561-564.

Zarković, K., Ilić, T., Savić, M., and Litovski, V., "*ANN application in Modeling of Dynamic Two-Terminal Linear Circuits*", Proceeding of ETAI'2000, September 2000, Ohrid, pp. E-27-E-30.

Jakovljević, M., Fotiu, P., Mrčarica, Ž., Litovski, V., and Detter, H., "*Electro-thermal simulation of microsystems with mixed abstraction modelling*", Proceedings of SSSS2000, Niš, Sept. 2000, pp. 13-22.

Ilić, T., Zarković, K., Litovski, V., and Damper, R., "*ANN Application in Modelling of Dynamic Linear Circuits*", Proceedings of SSSS2000, Niš, Sept. 2000, pp. 43-47.

Stefanović, D., Sokolović, M., Petković, P., and Litovski, V., "*T-Spice-Alecsis Co-simulation*", Proceedings of MIEL'02, Niš, 2002, pp. 625-628.

Andrejević, M., Milovanović, D., Petković, P., Litovski, V., "*Extraction of Frequency Characteristics of Switched-Capacitor Circuits Using Time-Domain Analysis*", Proceedings of MIEL'02, Niš, 2002 pp. 635-638.

Andrejević, M., Litovski, V., "*Electronic Modelling Using ANNs For Analogue and Mixed-Mode Behavioural Simulation*", Proc. of Neurel 2002, 2002, pp. 113-118.

Andrejević, M., Litovski, V., "*ANN Application In Modelling of Chua's Circuit*", Proc. of Neurel 2002, Belgrade, September 2002, pp. 119-122.

Andrejević, M., Litovski, V., "*Non-Linear Dynamic Network Modelling Using Neural Networks*", Int. Congress on Computational and Applied Mathematics, Leuven, Belgium, 22.July-26.July, 2002, pp. 16.

Savić, M., Litovski, V., "*Frequency Domain Electronic Circuit Analysis in an Object Oriented Environment*", Proc. of the 11th Int. Scientific and Appl. Sci. Conf. of Electronics ET2002, Sep., 2002, Sozopol, 2002. Vol. 2, pp. 65-70

Litovski V., Andrejević M., Damper. R., "*Modeling the d/a interface for mixed-mode behavioral simulation*", EUROCON 2003, Ljubljana, Sept. 2003, pp. A130-A133

Foreign papers

Mrčarica, Ž., Litovski, V., "A New Ideal Switch Model For Time-Domain Circuit Analysis", International Journal of Electronics, Vol. 74, No. 2, 1993, pp. 241-250.

Mrčarica, Ž., Vujanić, A., Detter, H., Litovski, V., "Simulation Of Micromechanical Systems", 6th International Journal of Theoretical Electrotechnics, Thessaloniki, Greece, Sep., 1996, pp. 141-148.

Litovski, V., Mrčarica, Ž., Ilić, T., "Simulation Of Non-Linear Magnetic Circuits Modelled Using Artificial Neural Network", Journal Simulation Practice and Theory, Vol. 5, 1997, pp. 553-570.

Maksimović, D., Litovski, V., "Logic Simulation Based Method For Digital CMOS VLSI Circuits Power Estimation ", 7th International J. Of Theoretical Electrotechnics, Join to ISTET '97, Cottbus, 1999, pp. 40-43.

Maksimović, D., Litovski, V., "Tuning The Logic Simulator For Timing Analysis", Electronics Letters, Vol. 35, No. 10, 13th May, 1999, pp. 800-802.

Mrčarica, Ž., Ilić, T., Litovski, V., "Time-Domain Analysis Of Nolinear Switched Networks With Internally Controlled Switches", IEEE Transactions on Circuits and Systems I, Vol. 46, No. 3, March, 1999, pp. 373-378.

Jakovljević, M., Mrčarica, Ž., Fotiu, P., Detter, H., Litovski, V., "Transient Electro-Thermal Simulation Of Microsystems With Space-Continuous Thermal Models In Analogue Behavioural Simulator", Microelectronics and Reliability, Vol. 40, No. 3, March 2000, pp. 507-516.

Ilić, T., Litovski, V., Litovski, I., Stojilković, S., "Computationally Efficient Large-Change Statical Analysis Of Linear Electronic Circuits", Microelectronics and Reliability, Vol. 40, 2000, pp. 1023-38.

Litovski, V., "New Methods in Modeling and Simulation of Electronic Circuits and Systems", Scientific Review, No. 29-30, 2001-2002, pp.189-207.

Maksimović, D., Litovski, V., "Logic simulation methods for longest path delay estimation", IEE Proceedings Computers and Digital Techniques, Vol. 149, No. 2, 2002, pp. 53-59.

Savić, M., Litovski, V., "Behavioral object-oriented frequency domain electronic simulation", Simulation News Europe, No. 38/39, December 2003, pp. 38-39.

Litovski, V. B., Andrejević, M., Petković, P., Damper, R., "ANN Application to Modelling of the D/A and A/D Interface for Mixed-Mode Behavioural Simulation", Journal of Circuits, Systems and Computers, Accepted for publication (January 2004).

Andrejević, M., Litovski, V., "Electronic Modelling Using ANNs For Analogue and Mixed-Mode Behavioural Simulation", Journal of Automatic Control, ETF Belgrade, accepted for publication

Hardware description languages

Foreign conferences

Damnjanović, M., Litovski, V., "CAD Systems Based On VHDL Description", 2nd Serbian Conf. on Microelectronics and Optoelectronics, MIOPEL '93, Niš, 26-28 October, 1993, pp. 443-448.

Dimić, Ž., Damnjanović, M., Glozić, D., Litovski, V., "VHDL Use For Alecsis Hybrid Simulation", Annual School - 17th International Spring seminar on Semiconductor and Hybrid Technologies 1994-1995, Sozopol, 1995, pp. 228-241.

Damnjanović, M., Dimić, Ž., Litovski, V., Glozić, D., "Hardware Description Language For Alecsis Simulator", 20th Int. Conf. on Microelectronics, MIEL '95, Vol. 2, Niš, 12-14 Sep., 1995, pp. 525-528.

Mrčarica, Ž., Litovski, V., Deliđ, N., Detter, H., "Modelling Of Micromechanical Devices Using Hardware Description Language", 5th Int. Conf. and Exhibition on Micro, Electro, Opto, Mechanical Systems and Components, Microsystem Technologies '96, Potsdam, 17-19 September, 1996, pp. 293-298.

Mrčarica, Ž., Litovski, V., Jakovljević, M., Detter, H., "Hierarchical Modelling Of Microsystems In An Object-Oriented HDL", 21st Int. Con. on Microelectronics, MIEL '97, Vol. 2, Niš, Sep., 1997, pp. 475-478.

Dimić, Ž., Damnjanović, M., Litovski, V., "VHDL - Alec++ Cosimulation", 21st Int. Conf. on Microelectronics, MIEL '97, Vol. 2, Niš, Sep., 1997, pp. 725-728.

Mrčarica, Ž., Maksimović, D., Tasić, A., and Litovski, V., "Some features of analogue and Mixed-Signal HDL AleC++", Proceedings of SSSS2000, Niš, September 2000, pp. 37-42.

Maksimović, D., Litovski, V., "Timing Simulation With VHDL Simulators", Proceedings of MIEL'02, Niš, 2002, pp. 655-658.

Litovski, V., Damnjanović, M., Andjelković, B., "ALECSIS/VHDL-AMS Mixed Language Simulation", Proc. of The 11th Int. Scientific and Appl. Sci. Conf. Electronic ET2002, Sozopol, Sep., 2002. Vol. 1, pp. 25-30

Foreign papers

Mrčarica, Ž., Litovski, V., Detter, H., "Modelling And Simulation Of Microsystems Using Hardware Description Language", Research Journal on Microsystem Technologies, Vol. 3, No. 2, Feb., 1997, pp. 80-85.

Litovski, V., Dimić, Ž., Damnjanović, M., Mrčarica, Ž., "Electronic Circuit Simulation In A Mixed-Language Environment", Miroelectronics Journal, Vol. 29, No. 8, 1998, pp. 553-558.

Litovski, V., Maksimović, D., and Mrčarica, Ž., "Mixed-signal modelling with AleC++: Specific features of the HDL", Simulation Practice and Theory, Vol. 8, 2001, pp. 433-449.

Damnjanović, M., Anđelković, B., Litovski, V., "VHDL-AMS Compiler for Alecsis Simulation Environment", Electronics, Vol. 6, No. 2, December 2002, pp. 6-11.