Abstract— Power supply of telecommunication and IT devices are considered from the point of view of their power quality parameters. Quality indicators are defined intended to study the nonlinear nature of this kind of loads. These are based at the most advanced modern research in the field of nonlinear loads to the grid. A measurement setup is described being powerful, versatile but inexpensive and enabling both verification of compliance with existing standard and categorization within a series production. The measurement and data processing system was here implemented for characterization of power supplies and battery chargers of personal communication and IT devices which in fact are considered small loads. The goal of this choice was to expose the properties of this kind of loads and to give a picture of how “green” they are. A set of measurements was performed and measured data analyzed to show that no matter how small, this kind of loads are a burden to the quality of the power delivered to the customers and even to the functionality of the electrical grid as such.

Index Terms— power quality, power factor, nonlinear loads

I. INTRODUCTION

In our recent studies [1, 2] we were considering the interaction between two domains being of crucial importance for the development of modern world: electrical power systems and ICT systems. While the first are striving to reduce the carbon footprint by changing the very source of energy to be converted into electricity, and to rise efficiency by using more and more sophisticated means in electricity production, distribution and control. On the other hand, the ICT systems are being transformed from a marginal to one of the most important loads to the electrical grid. In that interaction between these two systems, as we stated in [1], much effort is devoted to efficiency, dependability, and availability of power supply, while the understanding of the term “quality” undergoes a serious change in recent decades.

Namely, modern society critically depends on a secure supply of high-quality electrical energy [3] but the views about what quality in this respect means is gradually changing due to the new ubiquitous loads emerging in the last decades. For example, businesses as large as billions of dollars are emerging based on loads being not conceivable in the not so far past [4]. The loads we are speaking about, even in the cases when special care was devoted to their design, behave as nonlinear ones. This means they impregnate harmonics into the current waveform distributed by the grid which is designed to operate at a single frequency. So, if there is high volume of harmonics and phase distortions, no matter the: efficiency, dependability, availability, stability etc. the quality is jeopardized.

These issues were discussed in a study [5], claiming that nowadays we are witnessing changes in the demand and energy use. In fact the new demand determines ‘new’ load characteristics and trends while changes in the nature of the aggregate utility load happen. All of that is mostly due to the electronic plug-ins that became ubiquitous. Among them we may list a large group of loads but, instead, we will look at them as AC-to-DC converters used both as power supplies and as battery chargers. In the application cases we have in mind here, they are used in personal ICT equipment. A common property of these loads is that they consume small power, usually less than 100 W, hence the name “small loads”. Nowadays it is probably not adequate to say that these are ubiquitous. In fact they are more of that since their numbers exceeded the human population by large margin and are growing much faster. These numbers give importance to these loads no matter how small a single item is in comparison to others used mainly for heating water, classical lighting or powering machines.

The question is how green they are and how shall we define the greenness of such devices. How do they influence the quality of the power delivered to the customers? To get quantitative answers one has to have both: means to measure and theory to process the measurement results so that practical indicators to be produced based on which aggregate statistical calculations and comparisons may be done.

In our previous research we were first developing a tool for efficient measurements that will allow for proper and complete characterization of the new emerging loads [2]. Namely we found that the tools for characterization of modern loads available on the market, most frequently, lack at least one of the following properties: low price, ability of implementation of complex data processing algorithms (versatility), ability to store and statistically analyze the measured data, and ability to communicate with its environment no matter how distant it is. All these were achieved by the system reported in [2, 6] and the measurement results demonstrated here were obtained by these tools.

Next, we implemented these tools for characterization of small loads. The results obtained, as reported in [7] and [8] for example, were, in some cases, surprisingly different from what expected. That stands for the power components which
are not the active power and for the abundance of harmonics. In [9] we demonstrated that based on the main’s current, by proper data processing, despite the complex signal transformation between the mains and the components of a computer via the power supply chain, one may deduce the activities within the computer. Even more, one may recognize a software running within the computer. Such an information is distributed via the grid.

Here we will for the first time summarize the theoretical background of all computations necessary to be performed for complete characterization of a small load. Then, we will demonstrate our new results in the implementation of the theory and the measurement tools on a set of AC-to-DC converters used as power supplies or battery chargers of telecommunication and IT devices.

The paper will be organized as follows. First a short description of the measurement experiment will be given. To preserve conciseness, for this purpose, we will mainly refer to our previous work. The definitions used in modern characterization of the main’s current, voltage, and power which are implemented by our system will be listed in the third section so enabling the main attention to be devoted to the set of measured results and their analysis, which will be given next.

II. MEASUREMENT OF SMALL LOADS

To establish a comprehensive picture about the properties of a given load one need to perform complete analysis of the current and voltage waveforms at its terminals. In that way the basic and the higher harmonics of both the current and the voltage may be found. More frequently, however, indicators related to the power are sought in order to quantitatively characterize the load. Namely, a linear resistive load will have voltage and current in-phase and will consume only real power. Any other load will deviate from this characterization and one wants to know the extent of deviation expressed by as much indicators as necessary to get a complete picture. In the Appendix A, definitions of most important indicators are given based on the literature. All these were implemented in our measuring system which will be shortly described in the next.

The solution, as described in full details in [8,10], is based on a real time system for nonlinear load analysis. The system is based on virtual instrumentation paradigm, keeping main advantage of legacy instruments – determinism in measurement. The hardware part of the system is implemented using field programming gate array (PXI chassis equipped with PXI-7813R FPGA card with Virtex II FPGA) in control of data acquisition [11]. The software subsystem is implemented in two stages, executing on real-time operating system (PharLap RTOS) and general purpose operating system (GPOS). Described system enables calculation of a number of parameters in real-time that characterize nonlinear loads, which is impossible using classical instruments. The measured quantities are calculated from the current and voltage waveforms according to IEEE 1459-2000 [12] and IEEE 1459-2010 standards [13].

The system consists of three subsystems: acquisition subsystem, real time application for parameter calculations and virtual instrument for additional analysis and data manipulation. Acquisition subsystem consists of NI 9225 [14] and NI 9227 [15] c-series acquisition modules for A/D conversion connected to PXI-7813R FPGA card [2]. A/D resolution is 24-bit, with 50 kSa/s sampling rate and dynamic range ±300 V for voltages and ±5 A for currents.

III. DEFINITIONS OF THE QUALITY INDICATORS OF AN ELECTRONIC LOAD

A. Linear loads with sinusoidal stimuli

A sinusoidal voltage source

\[ v(t) = V_{RMS} \sin(\omega t) \]

supplying a linear load, will produce a sinusoidal current of

\[ i(t) = I_{RMS} \sin(\omega t - \phi) \]

where VRMS is the RMS value of the voltage, IRMS is the RMS value of the current, \( \omega \) is the angular frequency, \( \phi \) is the phase angle and \( t \) is the time. The instantaneous power is

\[ p(t) = v(t) \cdot i(t) \]

and it can be represented as

\[ p(t) = 2 V_{RMS} I_{RMS} \sin \omega t \cdot \sin \omega t - \phi = p_p + p_q \]

Using trigonometric transformations we can write:

\[ p_p = V_{RMS} I_{RMS} \cos \phi \left(1 - \cos(2\omega t)\right) \]

and

\[ p_q = -V_{RMS} I_{RMS} \sin \phi \sin(2\omega t) = -Q \cdot \sin(2\omega t) \]
\[ P = V_{\text{RMS}} \cdot I_{\text{RMS}} \cdot \cos \varphi, \quad Q = V_{\text{RMS}} \cdot I_{\text{RMS}} \cdot \sin \varphi \]  
(7)

represent real (\(P\)) and reactive (\(Q\)) power.

It can be easily shown that the real power presents the average of the instantaneous power over a cycle:

\[ P = \frac{1}{T} \int_{t_0}^{t_0+T} v(t) \cdot i(t) \cdot dt \]  
(8)

where \(t_0\) is arbitrary time (constant) after equilibrium, and \(T\) is the period (20ms in European and 1/60s in American system, respectively).

The reactive power \(Q\) is the amplitude of the oscillating instantaneous power \(P_0\). The apparent power is the product of the root mean square value of current times the root mean square value of voltage:

\[ S = V_{\text{RMS}} \cdot I_{\text{RMS}} \]  
(9)

or:

\[ S = \sqrt{P^2 + Q^2}. \]  
(10)

Power factor is simply defined as the ratio of real power to apparent power \([12, 16]\):

\[ TPF = \frac{P}{S}. \]  
(11)

For pure sinusoidal case, using (7), (10) and (11) we can calculate:

\[ TPF = \cos \varphi. \]  
(12)

Nonlinear loads

In the presence of nonlinear loads, the system operates in non-sinusoidal condition and use of legacy parameters such as power factor, defined as cosine of phase difference, does not describe system properly. Traditional power system quantities such as effective value, power (active, reactive, apparent), and power factor need to be numerically calculated from sampled voltage and current sequences by performing DFT, FFT or Goertzel algorithm \([16]\).

The RMS value of some periodic physical entity \(X\) (voltage or current) is calculated according to the well-known formula \([18]\):

\[ X_{\text{RMS}} = \sqrt{\frac{1}{T} \int_{t_0}^{t_0+T} (x(t))^2 \cdot dt} \]  
(13)

where \(x(t)\) represents time evolution, \(T\) is the period and \(t_0\) is arbitrary time. For any periodic physical entity \(x(t)\), we can give Fourier representation:

\[ x(t) = a_0 + \sum_{k=1}^{\infty} \left( a_k \cdot \cos(k \omega t) + b_k \cdot \sin(k \omega t) \right) \]  
(14)

or

\[ x(t) = c_0 + \sum_{k=1}^{\infty} c_k \cdot \cos(k \omega t + \psi_k) \]  
(15)

where \(c_0 = a_0\) represents DC component, \(c_k = \sqrt{a_k^2 + b_k^2}\) magnitude of \(k\)th harmonic, \(\psi_k = \arctan \frac{b_k}{a_k}\) phase of \(k\)th harmonic and \(\omega = \frac{2\pi}{T}\), angular frequency.

Fourier coefficients \(a_k, b_k\) are:

\[ a_0 = \frac{1}{T} \int_{t_0}^{t_0+T} x(t) dt, \quad a_k = \frac{2}{T} \int_{t_0}^{t_0+T} x(t) \cdot \cos \left( \frac{2\pi k t}{T} \right) dt \]  
(16)

and

\[ b_k = \frac{2}{T} \int_{t_0}^{t_0+T} x(t) \cdot \sin \left( \frac{2\pi k t}{T} \right) dt. \]  
(17)

The RMS value of \(k\)th harmonic is

\[ X_{k,\text{RMS}} = \sqrt{c_k^2 + \frac{2}{T} \int_{t_0}^{t_0+T} x(t) \cdot \cos \left( \frac{2\pi k t}{T} \right) \cdot dt}. \]  
(18)

We can calculate total RMS value

\[ X_{\text{RMS}} = \sqrt{\sum_{k=1}^{\infty} X_{k,\text{RMS}}^2} = \sqrt{X_{1,\text{RMS}}^2 + X_{H,\text{RMS}}^2} \]  
(19)

where \(M\) is highest order harmonic taken into calculation. Index “1” denotes first or fundamental harmonic, and index “\(H\)” denotes contributions of higher harmonics.

Equations (13) – (19) need to be rewritten for voltage and current. Practically, we operate with sampled values and integrals (16) and (17) are transformed into finite sums.

For a single-phase system where \(k\) is the harmonic number, \(\varphi_k\) phase difference between voltage and current of \(k\)th harmonic and \(M\) is the highest harmonic, the total active power is given by:

\[ P = \sum_{k=1}^{M} I_{k,\text{RMS}} \cdot V_{k,\text{RMS}} \cdot \cos \varphi_k = P + P_H. \]  
(20)

The first addend in the sum (20), denoted with \(P_1\), is fundamental active power. The rest of the sum, denoted with \(P_H\), is harmonic active power \([17]\).

There are a number of reactive power definitions for non-sinusoidal conditions in order to characterize nonlinear loads and measure the degree of loads’ non-linearity \([18]\). As more general term, non-active power \(N\), was introduced. All definitions have some advantages over others. Although tend to be general, there is no generally accepted definition.

The most common definition of reactive power is Budeanu’s definition \([19]\), given by following expression for single phase circuit:

\[ Q_B = \sum_{k=1}^{\infty} I_{k,\text{RMS}} \cdot V_{k,\text{RMS}} \cdot \sin \varphi_k. \]  
(21)

Budeanu proposed that apparent power is consist of two orthogonal components, active power (20) and non-active power, which is divided into reactive power (21) and distortion power:

\[ D = \sqrt{S^2 - P^2 - Q_B^2}. \]  
(22)

It should be noted that the actual contribution of harmonic frequencies to active and reactive power is small (usually less than 3% of the total active or reactive power). The major contribution of higher harmonic to the power comes as distortion power.

The apparent power, for non-sinusoidal conditions conventionally denoted as \(U\), can be written:
\[ U^2 = I_{\text{1,RMS}}^2 V_{\text{1,RMS}}^2 + I_{\text{1,RMS}}^2 V_{\text{2,RMS}}^2 + \frac{V_{\text{1,RMS}}^2 I_{\text{1,RMS}}^2}{S_I} + \frac{V_{\text{2,RMS}}^2 I_{\text{2,RMS}}^2}{S_H} \]  
\[ + V_{\text{1,RMS}}^2 I_{\text{1,RMS}}^2 + \frac{V_{\text{2,RMS}}^2 I_{\text{2,RMS}}^2}{S_H} \]  
(23)

where \( S_I \) represents fundamental apparent power, \( D_N \) voltage distortion power, \( D_I \) current distortion power and \( S_H \) harmonic apparent power. \( S_I \) and \( S_H \) are

\[ S_I = P_{\text{1,RMS}}^2 + Q_{\text{1},RMS}^2, \quad S_H = \sqrt{P_{\text{H,RMS}}^2 + Q_{\text{H},RMS}^2 + D_H^2} \]  
(24)

where \( D_H \) represents harmonic distortion power. The total apparent power, denoted with \( U \), is

\[ U = \sqrt{P^2 + Q^2 + D^2}. \]  
(25)

We can also define non-active power \( N \), defined with equation

\[ N = \sqrt{Q^2 + D^2} \]  
(26)

and phasor power \( S \), defined in the same way apparent power for sinusoidal conditions (10). It is obvious that for sinusoidal conditions, apparent power and phasor power are equal, and (25) reduces to (10).

The total harmonic distortions, THD, are calculated from the following formula [12]:

\[ THD_I = \frac{I_{\text{1,RMS}}}{I_{\text{1,RMS}}} = \sqrt{\frac{1}{M-1} \sum_{j=2}^{M} I_{j,\text{RMS}}^2} \]  
(27)

and

\[ THD_V = \frac{V_{\text{1,RMS}}}{V_{\text{1,RMS}}} = \sqrt{\frac{1}{M-1} \sum_{j=2}^{M} V_{j,\text{RMS}}^2} \]  
(28)

where \( I_j, V_k \) \( j,k=1, 2, \ldots, M \) stands for the harmonic of the current or voltage. It can be shown that:

\[ D_I = P_{\text{1,RMS}} I_{\text{1,RMS}} = S_I \cdot THD_I \]

\[ D_V = V_{\text{1,RMS}} I_{\text{1,RMS}} = S_H \cdot THD_V \]

\[ S_H = S_I \cdot THD_I \cdot THD_V. \]  
(29)

Fundamental power factor or displacement power factor is given by the following formula:

\[ PF_1 = \frac{P}{S_I} = \cos \phi_1. \]  
(30)

Total power factor TPF [12], defined by equation (12), taking into calculation (11) and (23), is

\[ TPF = \frac{P}{U} = \frac{P_1 + P_H}{\sqrt{S_I^2 + D_I^2 + D_H^2 + U_{\text{H,RMS}}^2}} \]  
(31)

and substituting (29) and (30):

\[ TPF = \frac{\left( 1 + \frac{P_H}{P_1} \right) \cos \phi_1}{\sqrt{1 + \text{THD}_I^2 + \text{THD}_V^2 + (\text{THD}_I \cdot \text{THD}_V)^2}}. \]  
(32)

Total power factor can be represented as product of distortion power factor DPF and displacement power factor PF1, i.e. \( \cos \phi_1 \):

\[ TPF = DPF \cdot \cos \phi_1. \]  
(33)

Therefore, distortion power factor is [12]

\[ DPF = \frac{1 + P_H}{\sqrt{1 + \text{THD}_I^2 + \text{THD}_V^2 + (\text{THD}_I \cdot \text{THD}_V)^2}} \]  
(34)

In real circuits, \( P_H \ll P_1 \) and voltage is almost sinusoidal \((\text{THD}_V < 5\% \)), leading to simpler equation for TPF [12]:

\[ TPF = \frac{\cos \phi_1}{\sqrt{1 + \text{THD}_I^2}}. \]  
(35)

IV. MEASUREMENT RESULTS

As mentioned in the introduction, our goal is here to demonstrate a complete set of measurements and calculations for characterization of small loads. The indicators so obtained may be used for decision making of various kinds such as verification of compliance to some standards or categorization within quality frames. As small loads here we consider power supply devices or battery chargers in case of personal communication and computing devices.

The devices that will be measured were chosen at random while measurements were performed for different states of activity of their own load.

The measured results are shown in Table 1. European electrical greed is considered. All tested devices (tablet computer, mobile phone, laptop computer and cordless telephone) contain rechargeable batteries. Working conditions are standby (device turned off and battery not charging), working and charging (device turned on and battery charging) and charging only (device turned off and battery charging). A standalone battery charger is also tested.

Following values are measured and shown in table: voltage RMS (V), current RMS (I), frequency (f), cosine of 1st harmonic phase difference (cos \( \phi_1 \)), TPF – total power factor (%), distortion power factor (%), THDV – voltage total harmonic distortion (%), THDI – current total harmonic distortion (%), active power (P), Budeanu’s reactive power (QB), apparent power (U), Distortion power (D), non-active power (N), phasor power (S), first harmonic active power (P1) and higher harmonic active power (PH).

In the next we will pay some attention to the very results depicted in Table 1. Let's first have a glimpse at the distortions of the current (THDI). As can be seen even in the best cases the THDI is larger than 20%. There is a case, a mobile phone battery charger while charging, where the THDI is 154.51% which means the harmonics exceed by a large margin the fundamental. Note that this is not an isolated case. One may observe several THDI s of similar value. To summarize, THDI is exposing the nonlinear character of all working and charging (device turned on and battery charging) and charging only (device turned off and battery charging). A standalone battery charger is also tested.

Following values are measured and shown in table: voltage RMS (V), current RMS (I), frequency (f), cosine of 1st harmonic phase difference (cos \( \phi_1 \)), TPF – total power factor (%), distortion power factor (%), THDV – voltage total harmonic distortion (%), THDI – current total harmonic distortion (%), active power (P), Budeanu’s reactive power (QB), apparent power (U), Distortion power (D), non-active power (N), phasor power (S), first harmonic active power (P1) and higher harmonic active power (PH).

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The next important and also interesting set of data is related to the power factor. In early days it was known as cos $\phi$ of the load while only linear loads were considered supposedly having reactive component introducing phase shift between the voltage and the current. The total power factor (TPF) encompasses the whole event including the distortions of both the voltage and the current and their mutual phase shift. As can be seen from Table 1, there is only one case where the TPF is approaching unity which is supposed to be its ideal value. In many of the cases the value of TPF is smaller than 50% meaning that the active power is smaller than a half of the total power drawn from the main which, as we could see from the previous paragraph, is mainly due to the distortions. In general, since most of the chargers are considered of small power (look to the column No. 7), the number of such devices, being attached to the mains all the time, is not in the range of billion(s).

The next column, the distortion power factor (DPF),
represents the percentage of power taken by the harmonics. As we can see, except for a small number of cases where the harmonics are approximately on the level of half of the total power, in most cases they are taking as large a power as the fundamental. Note, the harmonics are unwanted not only because of efficiency problems. In fact, in the long term, the presence of harmonics on the grid can cause [20]:

- Increased electrical consumption
- Added wear and tear on motors and other equipment
- Greater maintenance costs
- Upstream and downstream power-quality problems,
- Utility penalties for causing problems on the power grid
- Overheating in transformers, and similar.

Similar conclusion may be drawn in by comparison of the Distortion (D) and the power of the first (fundamental) harmonic (P1). There are only three cases where the second is larger than the former.

To summarize the data from Table 1 one may say that an electronic load to the grid which in fact represents a power supply of a telecommunication or IT device, represents a small but highly nonlinear load. In many cases the TPF of such a load is in favor of everything but not the active power to be delivered to the device.

V. CONCLUSION

The nature of the electrical loads to the grid is changing nowadays due to introduction of electronic apparatus being they home entertainment, home appliances, personal telecommunication or personal IT devices. All these devices behave as nonlinear loads to the grid which is a new state that in general may have damaging effects to the grid as such, to some of its components and, not to forget, to the very electronic loads being connected to it.

Having a right picture on the properties of the load is enabled by two factor: measurement system and signal processing system.

In these proceedings we represent our results in measuring and signal processing intended to characterize small loads. The importance of these results comes from the fact that the number of devices of this type connected to the grid globally exceeds billions at any time so producing losses and damage to the quality.

Having all these in mind the importance of the improvement of the performance of these devices comes in focus. That would lead to greener electronics i.e. ICT. We really hope that by offering a quality tool for characterization of these devices and by reporting the results which are exposing their weaknesses one may contribute to better designs or even more restrictive standards in this area.

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