Updated
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Personal information

Name / Surname	**Dejan Mirkovic**
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Nationality	Serbian
Date of birth	May 12, 1982
Gender	Male

Work experience

Dates	***April 2008 - Present***
Occupation or position held	Assistant professor (March 2021), Assistant (March 2011), Teaching assistant (April 2008) and Researcher
Main activities and responsibilities	– Giving lectures, auditory and laboratory exercises in courses: Electronics Basics, Digital Electronics, RF Electronics, Analog Integrated Circuits Design, Digital Integrated Circuits Design, Communication Circuits and Systems at BSc level and Design of the Mixed-Signals circuits at MSc level of studies. – Mentoring students at BSc and MSc level. – Design of hardware/software educational systems for laboratory exercises. – Administration and maintenance of the CAD/EDA tools for Integrated Circuits Design (Cadence Design System & Mentor Graphics). – Work on projects funded by the Ministry of Education, Science and Technological Development of Republic of Serbia. – Collaboration with Laboratory's industry partners though short-term projects. – Academic paper writing. – Review of the manuscripts for the national and international academic conferences and journals.

Name and address of employer	University of Nis, Faculty of Electronic Engineering, Aleksandra Medvedeva 14, 18000 Nis (Serbia)
Type of business or sector	Higher Education and Research
Dates	***February 2024 - January 2025***
Occupation or position held	Electronics & Seimcon Engineer
Main activities and responsibilities	Design and simulation of the analog IP blocks in 40nm CMOS: – Reference and Common-mode Voltage buffers for 12bit SAR ADC – Verliog-A modules for trimming of Band-Gap and Reference Buffer – Documentation Layout of Standard Cell Libraries in 130nm CMOS: – Layout modification of the standard cells for Leach-Up Effect prevention.
Name and address of employer	HDL Design House part of Capgemini, Bulevar Mihajla Pupina 115, 11000, Belegrade (Serbia)
Type of business or sector	IT & Semiconductor Engineering
Dates	***September 2019 - May 2020***
Occupation or position held	Analog & Mixed Signal Designer
Main activities and responsibilities	– PPG-ADC analog design and documentation. – PPG-ADC and ECG-ADC behavioral modeling and simulation with different digital filter architectures. – Design and verification of AS7050 sub-blocks (TIA, 8-bit DAC). – Performing block level and top level design verification.
Name and address of employer	ams Sensors Germany GmbH, Göschwitzer Straße 32, 07745 Jena, Germany
Type of business or sector	Semiconductors and Sensors
Dates	***November 2015 - January 2018***
Occupation or position held	Electronics Designer/Engineer
Main activities and responsibilities	– Design and verification of SPI block in LMS7002's FPRF Transceiver IC. – Back-end DRC and LVS of the LMS7002's MCU. – SystemC modeling and FPGA implementation of the Blind Source Separation (BSS) algorithm for Receiver IQ imbalance correction. – GUI controls for the Receiver IQ imbalance correction in the Lime Suite Desktop App for Lime's LMS SDR.
Name and address of employer	LIME MICROSYSTEMS DOO BEOGRAD-NOVI BEOGRAD, Bulevar Mihajla Pupina 10a/I, 11070 Novi-Beograd, Belgrade (Serbia)
Type of business or sector	Telecommunications and RF Systems
Dates	***November 2007 - April 2008***
Occupation or position held	Software Developer
Main activities and responsibilities	Maintenance and development of the PC App (C#) and Data base (Oracle 10) in Automatic Meter Reading (AMR) System for electricity consumption monitoring.
Name and address of employer	ATLAS ELECTRONICS DOO, Bulevar Svetog Cara Konstantina 80-82, 18106 Niš-Mediana (Serbia)[1]
Type of business or sector	Smart Electricity Metering

[1] Note: ATLAS ELECTRONICS DOO is now EWG DOO Niš

Education and training

Dates	***2015 - 2018***
Title of qualification awarded	PhD in Electrical Engineering and Computing
Thesis Title	Design of selective IIR digital filters with linear phase utilizing analog prototypes
Name and type of organization providing education and training	University of Nis, Faculty of Electronic Engineering
Level in national or international classification	ISCED8

Dates	***2001 - 2007***
Title of qualification awarded	Dipl. Ing. in Electrical Engineering for Electronics
Thesis Title	Design of the logic for controlling the EEPROM over I2C bus
Name and type of organization providing education and training	University of Nis, Faculty of Electronic Engineering
Level in national or international classification	ISCED7

Personal skills and competences

Mother tongue

Serbian

Other language(s)

Self-assessment *European level*(*)	**Understanding**		**Speaking**		**Writing**
	Listening	Reading	Spoken interaction	Spoken production	
English	C1	C1	B2	B2	B2

(*) *Common European Framework of Reference* (CEF) level

Social skills and competences

Organisational skills and competences

Organizing of academic conferences

Technical skills and competences

- Circuit simulation: Spectre, Hspice, Eldo, LTspice, PSpice.
- RTL simulation: ModelSim/Questa, NCsim.
- Mix-Signal simulation: Questa ADMS, AMS Designer, Dolphin SMASH.
- ASIC Design: Cadence Design System, Synopsys, Mentor Graphics.
- Process Design Kits (PDK): Cadence (AMIS 0.35u, TSMC 65n, ams AG 180n), Synpsys (KF 130nm, TSCM 40nm), MentorGraphcs (ADK 3.0/3.1, GDK 130n [educational only]).
- FPGA Design: Altera Quartus (Intel Quartus Prime).
- PCB Design: Cadence OrCAD, Altium Designer.

Computer skills and competences

- General Purpose: C/C++, VBA, Python.
- Scripting: Tcl, bash, Python, OCEAN/Skill (Candence), Ample (MentorGraphics).
- Numerc & Symbolic analysis: Matlab/GNU Octave, Python (Pylab), wxMxima.
- System level modelling: Simulink, LabVIEW, SystemC-AMS, Verilog-AMS, VHDL-AMS.
- RTL modeling: VHDL, Verilog, SystemC, Python (myHDL).
- Circuit Analysis: SPICE.
- Typesetting & Markup: TeX/LaTeX, HTML, CSS.

Artistic skills and competences

Amateur guitar playing (Music), Taekwondo (Martial Arts), drawing

Driving licence

B

Additional information

Projects funded by the Ministry of Education, Science and Technological Development of Republic of Serbia:

- TR32004 Advanced technologies for measurement, control, and communication on the electric grid (2011–2019)
- EF-PVT EK Design, verification and testing of electronic circuits, higher education development support (2018-1019)
- TR11007.A Design of solid state energy meter with protection of data in the system of consumption control and charging (2008–2011)

For full list of publications please visit LEDA publication database.

Personal interests

Music and Martial Arts